

Vishay Siliconix

P-Channel 60-V (D-S) MOSFET

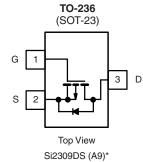
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 60	0.340 at V _{GS} = - 10 V	- 1.25		
	0.550 at V _{GS} = - 4.5 V	- 1		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Available
- TrenchFET[®] Power MOSFET



HALOGEN FREE Available



Si2309DS (A9)* * Marking Code

Ordering Information: Si2309DS-T1 Si2309DS-T1-E3 (Lead (Pb)-free) Si2309DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise n	oted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 60	v	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _{.I} = 150 °C) ^{a, b}	T _A = 25 °C	- I _D	- 1.25		
Continuous Drain Current $(1_J = 150^{\circ}C)^{23}$	T _A = 70 °C		- 0.85	^	
Pulsed Drain Current		I _{DM}	- 8	A	
Avalanche Current	L = 0.1 mH	I _{AS}	- 5		
Maximum Power Dissipation ^{a, b}	T _A = 25 °C	PD	1.25	W	
	T _A = 70 °C	ГD	0.8		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	- R _{thJA}		100	
Maximum Junction-to-Ambient ^a	Steady State		130	166	°C/W
Maximum Junction-to-Lead ^a	Steady State	R _{thJL}	45	60	

Notes:

a. Surface Mounted on FR4 board.

b. t \leq 5 s.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{DS} = 0 V$, $I_{D} = -250 \mu A$	- 60			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1				
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -48 V, V_{GS} = 0 V$		- 1			
		V_{DS} = - 48 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \geq$ - 4.5 V, V_{GS} = - 10 V	- 6			А	
Drain-Source On-State Resistance ^a	Б	V _{GS} = - 10 V, I _D = - 1.25 A		0.275	0.340	Ω	
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1 A		0.406	0.550		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 4.5 V, I _D = - 1 A		1.9		S	
Dynamic ^b				•			
Total Gate Charge	Qg			5.4	12	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 30 V, V_{GS} = - 10 V, I_{D} = - 1.25 A		1.15			
Gate-Drain Charge	Q _{gd}			0.92			
Turn-On Delay Time	t _{d(on)}			10.5	20		
Rise Time	t _r	V_{DD} = - 30 V, R_L = 30 Ω		11.5	20	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_G = 6 Ω		15.5	30		
Fall Time	t _f			7.5	15		
Source-Drain Rating Characteristics	b						
Continuous Current	۱ _S				- 1.25	А	
Pulsed Current	I _{SM}				- 8	~	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.25 A, V _{GS} = 0 V		- 0.82	- 1.2	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.25 A, dl/dt = 100 A/μs		30	55	ns	

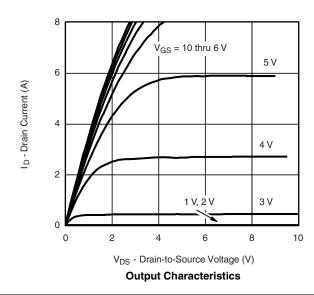
Notes:

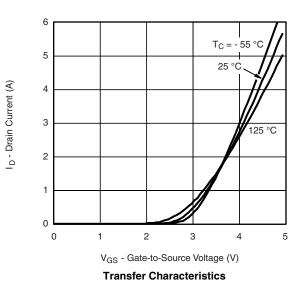
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



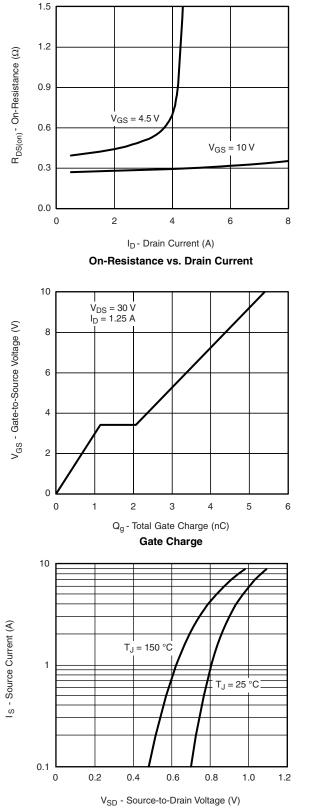


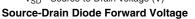


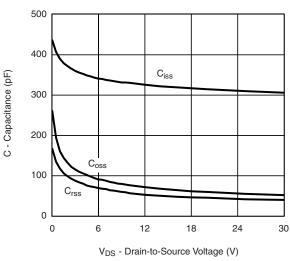
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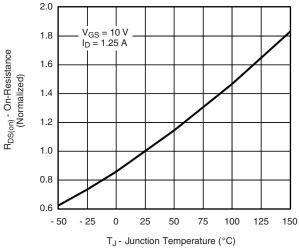
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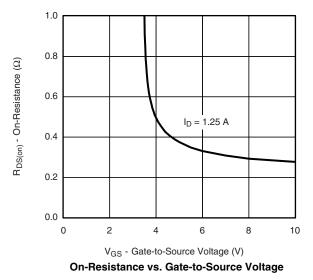




Capacitance



On-Resistance vs. Junction Temperature



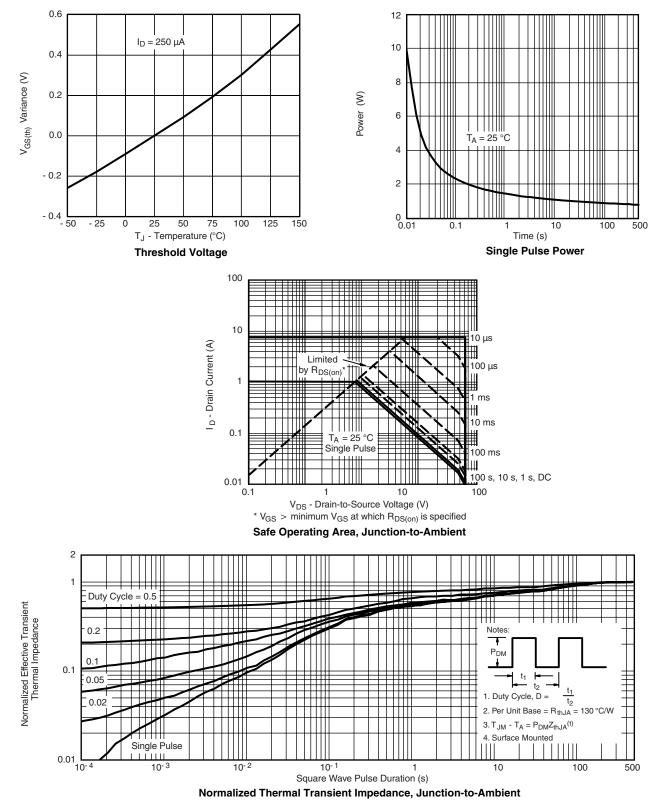
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