

<b>PCN Number:</b>	20200217000	<b>PCN Date:</b>	Feb 25, 2020
<b>Title:</b>	Datasheet for DS90UB933-Q1, DS90UB913A-Q1		
<b>Customer Contact:</b>	PCN Manager	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**DS90UB933-Q1**

SNLS546D – AUGUST 2016 – REVISED JANUARY 2020

Changes from Revision C (November 2019) to Revision D	Page
• Clarified GPO2 description by removing statement about leaving pin open if unused .....	4
• Added maximum power up timing constraint between VDD_n and PDB .....	35
• Added recommended software programming steps if VDD_n to PDB maximum power up timing constraint can not be met .....	36



**DS90UB913A-Q1**

SNLS443F – MAY 2013 – REVISED JANUARY 2020

Changes from Revision E (September 2018) to Revision F	Page
• Clarified GPO2 description by removing statement about leaving pin open if unused .....	6
• Added register 0x27[5] to register map .....	35
• Fixed missing register 0x29 typo .....	36
• Added maximum power up timing constraint between VDD_n and PDB .....	37
• Added recommended software programming steps if VDD_n to PDB maximum power up timing constraint can not be met .....	38

The datasheet number will be changing.

Device Family	Change From:	Change To:
DS90UB933-Q1	SNLS546C	SNLS546D
DS90UB913A-Q1	SNLS443E	SNLS443F

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/DS90UB933-Q1>

<http://www.ti.com/product/DS90UB913A-Q1>

#### Reason for Change:

To accurately reflect device characteristics.

#### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

#### Changes to product identification resulting from this PCN:

None.

#### Product Affected:

DS90UB913ATRTRVQ1	DS90UB913ATRTRVJQ1	DS90UB913ATRTRVTQ1	DS90UB933TRTRVQ1
DS90UB933TRTRVTQ1			

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

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