

PCN Number:	20180605000	PCN Date:	June 07, 2018
Title:	Datasheet for CDCE813-Q1		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



CDCE813-Q1

SNAS705A – JANUARY 2017 – REVISED MAY 2018

Changes from Original (January 2017) to Revision A

Page

- Changed the *Factory Default Setting* table and *Default Configuration* graphic text to show that S0 = 1 means Y1 outputs 3-state and S0 = 0 means Y1 is enabled 13



CDCE813-Q1

SNAS705B – JANUARY 2017 – REVISED MAY 2018

Changes from Revision A (May 2018) to Revision B

Page

- Changed the text in the *Default Device Configuration* section from: However the outputs are disabled by default and need to be turned on through I2C or with the S0 pin to: However the outputs are disabled by default and need to be turned on through I2C. 12
- Changed the *Factory Default Setting* table and *Default Configuration* graphic text to show that the Y1 outputs as 3-state when S0 = 1 or S0 = 0..... 13
- Changed the default value of the SLAVE_ADR 1:0 bits from: 00b to: 01b in the *Generic Configuration Register* table 16
- Changed the default value of the Y1_ST0 3:2 bits from: 11b to: 01b in the *Generic Configuration Register* table 16
- Changed the default value of the BCOUNT 7:1 bits from: 20h to: 00h in the *Generic Configuration Register* table 16
- Changed the default value of the Y2Y3_1 bit from: 1b to: 0b in the *PLL1 Configuration Register* table 17

The datasheet number will be changing.

Device Family	Change From:	Change To:
CDCE813-Q1	SNAS705	SNAS705B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/CDCE813-Q1>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

CDCE813QPWRQ1			
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com