

<b>PCN Number:</b>	20180809002.1	<b>PCN Date:</b>	Aug 13, 2018
<b>Title:</b>	Transfer of select C10 devices from ANAM-1 to DMOS5 Wafer Fab site		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Proposed 1<sup>st</sup> Ship Date:</b>	Nov 13, 2018	<b>Estimated Sample Availability:</b>	Date provided at sample request.
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Assembly Process
<input type="checkbox"/>	Design	<input type="checkbox"/>	Electrical Specification
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Packing/Shipping/Labeling
<input checked="" type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Wafer Bump Material
<input checked="" type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Part number change

**PCN Details**

**Description of Change:**

This change notification is to announce the transfer of select devices from ANAM-1 to the DMOS5 Wafer Fab site. Fab support from ANAM-1 has been discontinued. Buffer inventory has been built to cover the notification period of this change notification.

Current (Discontinued)				New (Transfer to Location)			
Current Fab Site	Fab Process	Probe Site	Wafer Diameter	New Fab Site	Fab Process	Probe Site	Wafer Diameter
ANAM-1	C10	DBUMP	200mm	DMOS5	C10	EBT	200mm

**Reason for Change:**

Discontinued Fab support from ANAM-1

**Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):**

None

**Changes to product identification resulting from this PCN:**


**Current:**

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
ANAM-1	ANM	KOR	Bucheon-si

**New:**

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
<b>DP1DM5</b>	<b>DM5</b>	<b>USA</b>	<b>Dallas</b>

Sample product shipping label (not actual product label)

(1P) SN74LS07NSR  
 (Q) 2000 (D) 0336  
 (31T) LOT: 3959047MLA  
 (4W) TKY (1T) 7523483SI2  
 (P)  
 (2P) REV: (V) 0033317  
 (20L) CS0: SHE (21L) CCO: USA  
 (22L) AS0: MLA (23L) ACU: MYS

MADE IN: Malaysia  
 2DC: 20:  
 MSL '2 /260C/1 YEAR SEAL DT  
 MSL 1 /235C/UNLIM 03/29/04  
 OPT:  
 ITEM: 39  
**LBL: 5A (L)T0:1750**

**Product Affected:**

TLK1501IRCP	TLK1501IRCPR	TLK2501IRCP	TLK2501IRCPR
TLK1501IRCPG4	TLK1501IRCPRG4	TLK2501IRCPG4	TLK2501IRCPRG4

## Qualification Report

### TLK1501IRCP C10 Offload from Dongbu to DMOS5 Approve Date 24-July-2018

#### Product Attributes

Attributes	Qual Device: TLK1501IRCP	QBS Process Reference: TLK2500IRCP	QBS Package Reference: TLK2201BIRCP
Wafer Fab Supplier	DMOS5	DMOS5	DMOS5
Wafer Process	25C10	25C10	25C10
Assembly Site	TAI	TAI	TAI
Package Family	HVQFP	HVQFP	HVQFP
Flammability Rating	UL 94 V-0	UL 94 V-0	UL 94 V-0

- QBS: Qual By Similarity

- Qual Device TLK1501IRCP is qualified at LEVEL3-260C

#### Qualification Results

##### Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TLK1501IRCP	QBS Process Reference: TLK2500IRCP	QBS Package Reference: TLK2201BIRCP
AC	Autoclave 121C, 2atm	96 Hours	-	3/231/0	3/231/0
CDM	ESD - CDM	1000 V	1/3/0	-	-
ED	Electrical Characterization	Per Data Sheet Parameters	Pass	-	-
HAST	Biased HAST, 130C/85%RH	96 Hours	-	3/231/0	-
HBM	ESD - HBM	4000 V	1/3/0	-	-
HTOL	Life Test, 155C	240 Hours	-	3/231/0	-
HTSL	High Temp. Storage Bake, 170C	420 Hours	-	3/135/0	3/135/0
LU	Latch-up	( Per JESD78 )	1/6/0	-	-
T/C	Temp Cycle -65C/150C	500 Cycles	-	-	3/231/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours

- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

#### Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

## Qualification Report

### TLK2501IRCP C10 Offload from Dongbu to DMOS5 Approve Date 24-July-2018

#### Product Attributes

Attributes	Qual Device: TLK2501IRCP	QBS Process Reference: TLK2500IRCP	QBS Package Reference: TLK2201BIRCP
Wafer Fab Supplier	DMOS5	DMOS5	DMOS5
Wafer Process	25C10	25C10	25C10
Assembly Site	TAI	TAI	TAI
Package Family	HVQFP	HVQFP	HVQFP
Flammability Rating	UL 94 V-0	UL 94 V-0	UL 94 V-0

- QBS: Qual By Similarity

- Qual Device TLK2501IRCP is qualified at LEVEL3-260C

#### Qualification Results

**Data Displayed as: Number of lots / Total sample size / Total failed**

Type	Test Name / Condition	Duration	Qual Device: TLK2501IRCP	QBS Process Reference: TLK2500IRCP	QBS Package Reference: TLK2201BIRCP
AC	Autoclave 121C, 2atm	96 Hours	-	3/231/0	3/231/0
CDM	ESD - CDM	1000 V	1/3/0	-	-
ED	Electrical Characterization	Per Data Sheet Parameters	Pass	-	-
HAST	Biased HAST, 130C/85%RH	96 Hours	-	3/231/0	-
HBM	ESD - HBM	4000 V	1/3/0	-	-
HTOL	Life Test, 155C	240 Hours	-	3/231/0	-
HTSL	High Temp. Storage Bake, 170C	420 Hours	-	3/135/0	3/135/0
LU	Latch-up	( Per JESD78 )	1/6/0	-	-
T/C	Temp Cycle -65C/150C	500 Cycles	-	-	3/231/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k hours, 140C/480 hours, 150C/300 hours, 155C/240 hours

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#### Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

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Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
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