Product Change Notification - SYST-27XALV070

Date:

28 Nov 2018

Product Category:

Ethernet Switches

Affected CPNs:

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Notification subject:

Data Sheet - KSZ8567R Data Sheet Data Sheet Document Revision

Notification text:

SYST-27XALV070

Microchip has released a new DeviceDoc for the KSZ8567R Data Sheet of devices. If you are using one of these devices please read the document located at KSZ8567R Data Sheet.

Notification Status: Final

Description of Change: Below are the changes

Section/Figure/Entry	Correction
Section 8.1, "Package Mark- ing Information," on page 223	Updated top marking information.
Section 8.2, "Package Draw- ings," on page 224	Updated package drawings.
Table 4-2, "Enabling and Disabling Quiet-WIRE"	Updated MMD Quiet-WIRE Configuration 3 Register “Disable Quiet-WIRE” entry.
Table 4-17, "Matching Rule Options"	Table updated.
Section 4.4.9, "Tail Tagging Mode," on page 37	Section updated. Added PTP specific content.
Section 4.1.8, "LinkMD®+ Enhanced Diagnostics: Receive Signal Quality Indi- cator," on page 22	Updated LinkMD+ text.
Cover, Section 4.0, "Func- tional Description," on page 18, Section 5.0, "Device Registers," on page 67	Removed LinkMD references.
Section 4.1.6, "Quiet-WIRE Filtering," on page 21, Sec- tion 5.4, "MDIO Manage- able Device (MMD) Registers (Indirect)," on page 197	Updated functional description and added Quiet-Wire register descriptions.
Section 4.4.15, "Low Latency Cut- Through Mode," on page 41	Minor text clarification.
Section 4.4.2.4, "Learning," on page 28	Text correction.
Section 4.4.2.6, "Aging," on page 29	Corrected “time stamp” to “age count” in multiple locations.
Section 4.2.2, "Tri-Color Dual-LED Mode," on page 24, Table 4-4, "Tri- Color Dual-LED Mode Pin Definition"	Removed errant 1000Mbps references.
Section 5.2.2.5, "PHY Auto- Negotiation Advertisement Register," on page 142	Changed default value of Pause (Flow Control) Capability bit to a note referencing the LED1_1 configuration strap.
Section 5.2.7.4, "Port Authentication Control Reg- ister," on page 168	Corrected bits 1:0 description.
Section 5.1.6.11, "Global PTP Message Config 1 Reg- ister," on page 115	Corrected 802.3AS to 802.1AS and added descrip- tions.
Section 5.1.1.4, "Global Chip ID 3 Register," on page 70	Corrected bit 0 description.
Section 5.4, "MDIO Manage- able Device	Added definitions for MMD Signal Quality

(MMD) Regis- ters (Indirect)," on page 197	Register (ACh) and MMD Quiet-WIRE Configuration Regis- ters (25h-34h).
Section 5.4, "MDIO Manage- able Device (MMD) Regis- ters (Indirect)," on page 197	Corrected the MMD register read example.
Section 5.4.5, "MMD Quiet- WIRE Configuration 1 Reg- ister," on page 199, Section 5.4.6, "MMD Quiet-WIRE Configuration 2 Register," on page 199, Section 5.4.7, "MMD Quiet-WIRE Configu- ration 3 Register," on page 200	Updated default value fields.
Table 6-2, "RGMII Timing Values"	Revised minimum RGMII TSKEW parameter.
Table 3-3, "Configuration Strap Descriptions"	Corrected swapping of LED2_0 and LED4_0, added notes in strapping. Corrected RXD6_0 and RXD7_0 in strapping table.

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 27 Nov 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

KSZ8567R Data Sheet

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Affected Catalog Part Numbers (CPN)

KSZ8567RTXI
KSZ8567RTXI-TR
KSZ8567RTXV-TRV01
KSZ8567RTXV-TRV03
KSZ8567RTXV-TRVAO
KSZ8567RTXV-VAO