

PCN Number:	20170728000	PCN Date:	August 18, 2017
Title:	Datasheet for AM5726, AM5728		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



AM5728, AM5726

SPRS953C – DECEMBER 2015 – REVISED JUNE 2017

Changes from November 28, 2016 to June 6, 2017 (from B Revision (November 2016) to C Revision) Page

• Moved device hex code from TRM to DM	6
• Changed "No" in Device Comparison table to "Not Supported"	6
• Updated column description parameter [4] in Ball Characteristics Table	10
• Added a note to gpmc_a[22:19] and gpmc_a[27:24] signals about internal pulldowns activation	121
• Changed GPMC 1 Load / 5 Load to Default Mode / Alternate Mode in Table 4-29	150
• Added footnotes in Electrical Characteristics section clarifying VDDS in each table	185
• Added RC On-die Oscillator Clock section	205
• Changed GPMC 1 Load / 5 Load names to Default Mode / Alternate Mode	212
• Added new MANUAL3 and MANUAL4 timing mode options for DPI1/2/3 in Table 7-2	212
• Added mmc3_clk row into the Manual Function Mapping for VIP1 2B table	222
• Added DSS clock jitter footnotes on Table 7-15 and Table 7-16	233
• Added New DSS Timing Specification Tables for Manual 3 and Manual 4 Mode Operation	235
• Updated Timing specification values for McASP2 in Table 7-50	287
• Updated Virtual Mode Case Details for McASP2 in Table 7-56	293
• Removed McASP "ahclkx" signals from Table 7-63 and Table 7-64 to avoid confusion	300
• Updated HS200 timing diagrams (parameters HS2005 and HS2006)	330
• Removed 1149.7 (cJTAG) support	373
• Updated PDN application report link and literature number	399
• Added references to thermal application notes	399
• Added section LJCB_REFN/P Connections	401

The datasheet number will be changing.

Device Family	Change From:	Change To:
AM5726, AM5728	SPRS953B	SPRS953C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/AM5726>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:			
AM5726BABCX	AM5726BABCXA	AM5726BABCXAR	AM5726BABCXEA
AM5728BABCX	AM5728BABCXA	AM5728BABCXEA	XAM5728AABCXE
XAM5728BABCXE			

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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